			DD	l m2
L Number	Hits	Search Text	DB	Time stamp
1	48048	SRAM or (static adj5 random adj5 access adj5 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12
2	28	(SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12 00:41
3	6	((SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12
4	69	(SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
5	6	((SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
6	6	("true bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
7	16	("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
8	4	(("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
9	11	("bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
10	27	("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
11	4	(("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
12	75	bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:45
14	68	(bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 00:45

15	36		USPAT;	2004/07/12
		bit\$1lin\$1) and pre\$1charg\$3) and (FET or	US-PGPUB;	00:46
		"field effect transistor" or transistor))	EPO; JPO;	
		and SRAM	DERWENT;	
			IBM_TDB	
16	32		USPAT;	2004/07/12
		bit\$1lin\$1) and pre\$1charg\$3) and (FET or	US-PGPUB;	00:46
		"field effect transistor" or transistor))	EPO; JPO;	
		and SRAM) and 365/\$7.ccls.	DERWENT;	
			IBM_TDB	
17	5		USPAT;	2004/07/12
		bit\$1lin\$1) and pre\$1charg\$3) and (FET or	US-PGPUB;	00:46
		"field effect transistor" or transistor))	EPO; JPO;	
		and SRAM) and 365/154.ccls.	DERWENT;	
			IBM_TDB	
18	9	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2004/07/12
		bit\$1lin\$1) and pre\$1charg\$3) and (FET or	US-PGPUB;	00:46
		"field effect transistor" or transistor))	EPO; JPO;	j
		and SRAM) and 365/201.ccls.	DERWENT;	
			IBM_TDB	
19	0	adam-chad-a.in.	USPAT;	2004/07/12
			US-PGPUB;	00:47
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
20	0	adams-chad-a.in.	USPAT;	2004/07/12
			US-PGPUB;	00:47
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2004/07/10
21	0	aipperspach-anthony.in.	USPAT;	2004/07/12
			US-PGPUB;	00:47
1			EPO; JPO;	
			DERWENT;	
22	42		IBM_TDB USPAT;	2004/07/12
22	42	aipperspach-anthony-\$.in.	· ·	00:47
			US-PGPUB; EPO; JPO;	00.47
			DERWENT;	
			IBM TDB	j
23	o	christensen-todd.in.	USPAT;	2004/07/12
23	ا	CHITIS CERISER COUC. III.	US-PGPUB;	00:48
			EPO; JPO;	00.30
			DERWENT;	
			IBM TDB	
24	41	christensen-todd-\$.in.	USPĀT;	2004/07/12
			US-PGPUB;	00:48
			EPO; JPO;	
			DERWENT;	
1			IBM TDB	
25	o	freiburger-peter.in.	USPAT;	2004/07/12
			US-PGPUB;	00:48
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
26	. 12	freiburger-peter-\$.in.	USPAT;	2004/07/12
	_		US-PGPUB;	00:48
			EPO; JPO;	
			DERWENT;	
			IBM TDB	l l
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